

UNITED STATES PATENT AND TRADEMARK OFFICE  
**CERTIFICATE OF CORRECTION**

PATENT NO. : 6,862,281 B1  
DATED : March 1, 2005  
INVENTOR(S) : Chandrasekaran

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 2,

Line 13, replace "et a).," with -- et al., --.

Line 43, replace "(LA)" with -- (L4) --.

Column 3,

Line 8, replace "port Each" with -- port. Each --.

Line 32, replace "16 bit" with -- 16-bit --.

Column 4,

Line 25, replace "test Such" with -- test. Such --.

Column 6,

Line 53, replace "LA" with -- L4 --.

Column 7,

Line 21, replace "port" with -- port. --.

Column 10,

Line 11, replace "claim 10" with -- claim 17 --.

Line 53, delete "further" between "memory" and "comprises".

Column 12,

Line 8, replace "fist" with -- first --.

Signed and Sealed this

Twentieth Day of September, 2005



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JON W. DUDAS

*Director of the United States Patent and Trademark Office*